

17W 2195

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Sokolov et al.

Attorney Docket No.: SUN1P816/P5614

Application No.: 09/818,096

Examiner: SHAH, Nilesh R.

Filed: March 26, 2001

Group: 2195

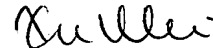
Title: LOADING ATTRIBUTE FOR PARTIAL
LOADING OF CLASS FILES INTO VIRTUAL
MACHINES

Confirmation No.: 2837

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on June 2, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____


Susan W. Xu

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of Foreign Patents or Published Foreign Patent Applications and Other Documents are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first

Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P816).

Respectfully submitted,
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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. SUN1P816/P5614	Application No.: 09/818,096
	Applicant: Sokolov et al. Filing Date March 26, 2001	Group 2195

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1	2002/0046298A1	04/18/02	Bak et al.			06/30/97
	A2	2002/0170041A1	11/14/02	Shann			12/20/01
	A3	2002/0170043A1	11/14/02	Bagley et al.			02/23/01
	A4	4,199,811	04/22/80	Borgerson et al.			09/02/77
	A5	4,667,290	05/19/87	Goss et al.			09/10/84
	A6	5,838,980	11/17/98	Guillen et al.			06/25/97
	A7	5,872,978	02/16/99	Hoskins			12/19/96
	A8	5,893,084	04/06/99	Morgan et al.			10/04/96
	A9	5,903,761	05/11/99	Tyma			10/31/97
	A10	5,970,242	10/19/99	O'Connor et al.			01/23/97
	A11	5,978,585	11/02/99	Crelier			03/27/97
	A12	6,026,485	02/15/00	O'Connor et al.			01/23/97
	A13	6,047,125	04/04/00	Agesen et al.			10/01/97
	A14	6,072,951	06/06/00	Donovan et al.			10/15/97
	A15	6,101,580	08/08/00	Agesen et al.			04/23/97
	A16	6,118,940	09/12/00	Alexander III et al.			11/25/97
	A17	6,182,202 B1	01/30/01	Muthukkaruppan			10/31/97
	A18	6,223,202 B1	04/24/01	Bayeh			06/05/98
	A19	6,260,187 B1	07/10/01	Cirne			08/20/98
	A20	6,324,685 B1	11/27/01	Balassanian			03/18/98
	A21	6,349,377 B1	02/19/02	Lindwer			09/28/98
	A22	6,374,286 B1	04/16/02	Gee et al.			04/06/98
	A23	6,412,108 B1	06/25/02	Blandy et al.			05/06/99
	A24	6,427,228 B1	07/30/02	Wigger			05/12/99
	A25	6,434,625 B1	08/13/02	Loen			07/13/99
	A26	6,446,254 B1	09/03/02	Chapman et al.			08/13/99
	A27	6,467,037 B1	10/15/02	Griesemer			06/30/98
	A28	6,477,702 B1	11/05/02	Yellin et al.			11/09/00
	A29	6,658,421 B1	12/02/03	Seshadri			02/14/00
	A30	6,684,394 B1	01/27/04	Shann			08/28/00
	A31	6,704,923 B1	03/09/04	Gosling			02/28/00
	A32	6,851,108 B1	02/01/05	Syme et al.			11/16/00
	A33	6,851,111 B2	02/01/05	McGuire et al.			12/15/00
Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	March 26, 2001	2195

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1	WO01/22213A2	03/29/01	PCT				
	B2	WO02/077807A1	10/03/02	PCT				
	B3	WO02/41145 A2	05/23/02	PCT				

Other Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	C1	Lambright H D, "Java Bytecode Optimizations" Proceedings of IEEE Compcon '97. San Jose, Feb. 23-26, 1997, pages 206-210, XP-000751784					
	C2	Accomazzo E et al., "Integrating intermediate Code Optimization with Retargetable Code Generation" Microprocessing and Microprogramming, Elsevier Science Publishers, BV. Amsterdam, NL, vol. 30, No. 1/5, August 1, 1990, pages 475-481, XP000141684					
	C3	Tim Ritchey, "Java!" 1995, New Riders Publishing, p, 326-343					
	C4	Microsoft Press Computer Dictionary Third Edition, p. 294-295					
	C5	Piumarta et al., "Optimizing Direct Threaded Code by Selective Inlining," Association for Computing Machinery, Vol. 33, No. 5, p. 291-300, May 1, 1998. XP-000766278					
	C6	Tommesani, "Programming models"					
	C7	Goldberg, "A specification of java loading and bytecode verification," ACM, p. 49-58, 1998					
	C8	Chang et al. "Type based verification of assembly language for compiler debugging," ACM TLDI, p. 91-102, Jan. 2005					
	C9	Qian et al., "A formal specification of Java class loading," ACM OOPSLA, p. 325-336, 2000					
	C10	Krintz et al., "Reducing transfer delay using java class file splitting and prefetching," ACM OOPSLA, pp276-291 11/1999					
	C11	Clausen et al., "Java Bytecode Compression for Low-End Embedded Systems," 5/2000, ACM					
	C12	Chilimbi et al., "Cache-Conscious Structure Definition", P. 13-24, 1999 ACM					
	C13	Nicol et al., "Empirical Study of parallel Trace-Driven LRU Cache Simulators," p.166-169, 1995 IEEE					
	C14-16	Office Actions dated 03/13/2003, 07/31/2003 and 02/05/04 respectively by Examiner Charles Roncs for U.S. Application No. 09/852,463, entitled "Frameworks for Accessing Java Class Files."					
Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.